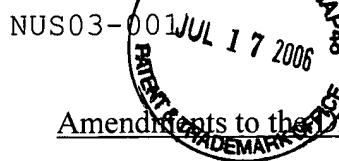


NUS03-001



Amendments to the Drawings

Serial No. 10/802, 563

The attached proposed sheets of drawings include changes to Figs. 7, 8, 9, and 10 and replace the original drawing sheets of Figs. 6-8 and Figs. 9-10. The dual gate electrodes of the CMOS structure are shown in Figs. 7-10 as requested by the Examiner. These figures are described on page 7. Note that "gate electrodes" are described.

Attachment: Replacement sheets

REMARKS

Examiner Su Kim is thanked for the thorough examination and search of the subject Patent Application and for finding all pending claims allowable.

The drawings are amended as requested by the Examiner and as described above.

It is requested that should Examiner Kim have any further concerns about the subject patent application, that the Examiner call the undersigned at 765 4530866 to overcome any problems preventing allowance.

Respectfully submitted,



Stephen B. Ackerman, Reg. No. 37,761

Attachment: Replacement sheets